

VOLTAGE LEVEL SHIFTER

ABSTRACT OF THE DISCLOSURE

A high-speed voltage level shifter. A transistor (10) may be connected to high voltage (VPP) and may act as a source of a limited current to a first node (21), and a driver (14, 15) connected to the first node may provide a level-shifted output signal (VOUT) to a memory control input line of a memory cell (6). A plurality of series-connected transistors (12A-12N) may be connected between a second node (22A) and a circuit ground, each transistor may have an input connected to a corresponding control signal (VIN-A to VIN-N) from a control circuit (5). A transistor (11) may be connected between the first node and the second node in a source-follower configuration and may have an input connected to a bias voltage (VBIAS) which may limit the voltage at node 22A, so transistors 12A-12N may be low-voltage, high speed transistors.